Application No. 09●●●50
Amendment dated ●●●mber 29, 2003
Reply to Final Office Action of September 29, 2003

REMARKS/ARGUMENTS

Applicant has carefully reviewed and considered the Office Action mailed on September 29, 2003, and the references cited therewith.

Claims 1, 8, 15 and 20 are amended, claims 5 and 9 are canceled, and no claims are added; as a result, claims 1-4, 6-8, and 10-20 are now pending in this application.

§103 Rejection of the Claims

Claims 1-6, 8-10, 12-14 and 16-18 were rejected under 35 USC § 103(a) as being unpatentable over Rademacher (U.S. Patent No. 5,930,466) in view of Bender, et al. (U.S. Patent No. 6,038,033).

Applicant's independent claim 1, as amended, recites in part "<u>wherein the printer controller and the buffer are embedded in a single ASIC</u>."

Applicant's independent claim 8, as amended, recites in part "wherein the printer further includes a printer controller for decompressing contents of the swath buffer, <u>the printer controller and the swath buffer being embedded in a single ASIC</u>."

Hence, each of the Applicant's independent claims 1 and 8 identify "<u>the printer controller and the buffer are embedded in a single ASIC</u>." Similar subject matter was original recited in Applicant's dependent claim 5. The Examiner's grounds for rejecting claim 5 stated:

> Rademacher discloses: The printer of claim 1, wherein the printer controller and the buffer being are embedded in a single ASIC (please note Fig. 1 items 38 and 46 and 36 column 8 lines 63-67 and column 9 lines 1-25)

Figure 1 illustrates RAM 36 separate from ASIC 38. Further Figure 1 illustrates input buffer 46 separate from ASIC 38. From the Applicant's review of the of the cited column and lines, the text appears to clearly state a separate input buffer 46 which is further connected to a data bus 48 that communicates the incoming print data from input buffer 46 to ASIC 38. There is no recitation or description of "<u>the printer controller and the buffer are embedded in a single ASIC</u>." Indeed, the Rademacher reference numerically illustrates and textually describes an input buffer located off of the ASIC as similarly is the RAM. The Bender reference

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does not cure the deficiencies of the Rademacher reference. That is, the Bender reference likewise does not teach or describe "the printer controller and the buffer are embedded in a single ASIC."

Accordingly, Applicant submits that each and every element and limitation of independent claims 1 and 8, as amended, is not taught or suggested by the references, either independently or in combination. As such, Applicant respectfully requests reconsideration and withdrawal of the 103 rejection for these independent claims, as well as for the claims which depend therefrom.

Claims 7, 11, 15, 19 and 20 were rejected under 35 USC § 103(a) as being unpatentable over Rademacher (U.S. Patent No. 5,930,466) in view of Bender, et al. (U.S. Patent No. 6,038,033) and further in view of Ueda, et al. (U.S. Patent No. 6,538,764).

For the reasons provided above, Applicant believes that independent claims 1 and 8 are in condition for allowance. The Ueda reference does not cure the deficiencies with the Rademacher and Bender references in connection with the 103 rejection. That is, nowhere in the Ueda does the reference describe, teach or suggest that "the printer controller and the buffer are embedded in a single ASIC." Therefore, each and every element and limitation of independent claims 1 and 8, as amended, is not taught or suggested by the references, either independently or in combination. As such, Applicant respectfully requests reconsideration and withdrawal of the 103 rejection for claims 7 and 11 which depend from allowable claims 1 and 8.

Applicant's independent claim 15, as amended, recites in part "using a swath buffer embedded in a print controller ASIC to buffer the compressed rows."

As noted above, the Applicant submits that neither Rademacher nor Bender describe, teach or suggest "using a swath buffer embedded in a print controller ASIC to buffer the compressed rows." Again, the Ueda reference does not cure the deficiencies with the Rademacher and Bender references in this regard. That is, no where in Ueda is "using a swath buffer embedded in a print controller ASIC to buffer the compressed rows" described. Therefore, each and every element and limitation of independent claim 15, as amended, is not taught or suggested by the references, either independently or in combination. As such, Applicant respectfully

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requests reconsideration and withdrawal of the 103 rejection for independent claim 15 as well as for claim 19 which depends therefrom.

Applicant's independent claim 20, as amended, recites in part "<u>data for causing the processor to determine a number of complete compressed rows that can fit in a swath buffer embedded in a print controller ASIC</u>."

Neither Rademacher nor Bender describe, teach or suggest "data for causing the processor to determine a number of complete compressed rows that can fit in <u>a swath buffer embedded in a print controller ASIC</u>." Once again, the Ueda reference does not cure the deficiencies with the Rademacher and Bender references in this regard. That is, no where in Ueda is "<u>data for causing the processor to determine a number of complete compressed rows that can fit in a swath buffer embedded in a print controller ASIC</u>" described. Therefore, each and every element and limitation of independent claim 20, as amended, is not taught or suggested by the references, either independently or in combination. As such, Applicant respectfully requests reconsideration and withdrawal of the 103 rejection for independent claim 20.

CONCLUSION

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (360) 212-0769 to facilitate prosecution of this matter.

If necessary, please charge any additional fees or credit overpayment to the Deposit Account No. 08-2025.

CERTIFICATE UNDER 37 CFR §1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS NON-FEE AMENDMENT Commissioner for Patents, P.O. BOX 1450, Alexandria, VA 22313-1450 on this **29** day of **December**, 2003.

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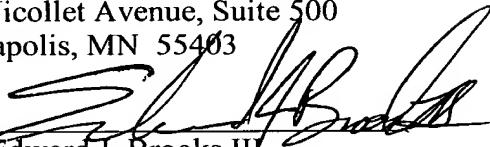Name

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